

POTENTIAL +/- DIFFERENCE INC

BITT T3 TEST DATA

October 2nd, 2010

Test Overview: Previous IP iterations of the BITT employed a high impedance Primary Coil as deterrent to a Secondary BEFF induced flux. This test data pertains to a low impedance Primary Coil operated at **saturation** as a deterrent to Secondary Coil BEMF induced flux.

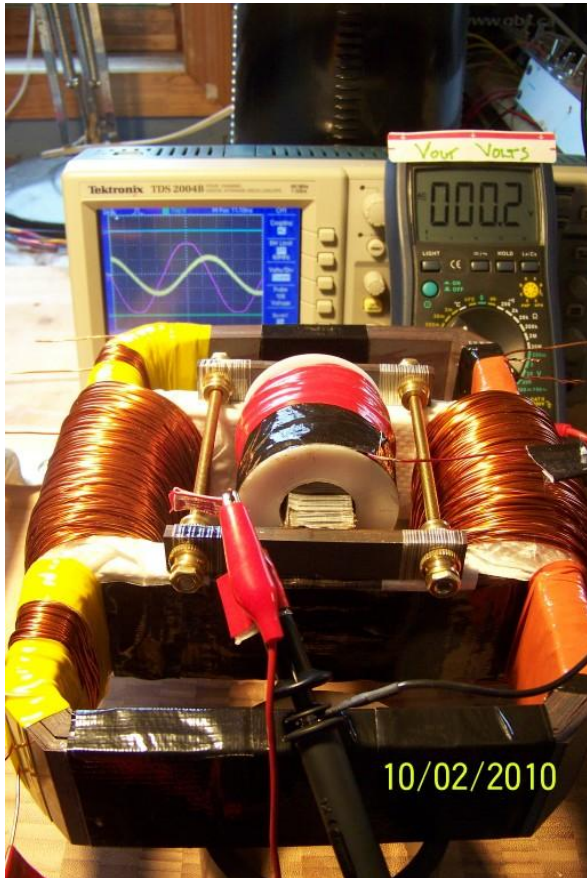


FIG 1.0 BITT T3
Low Output Voltage

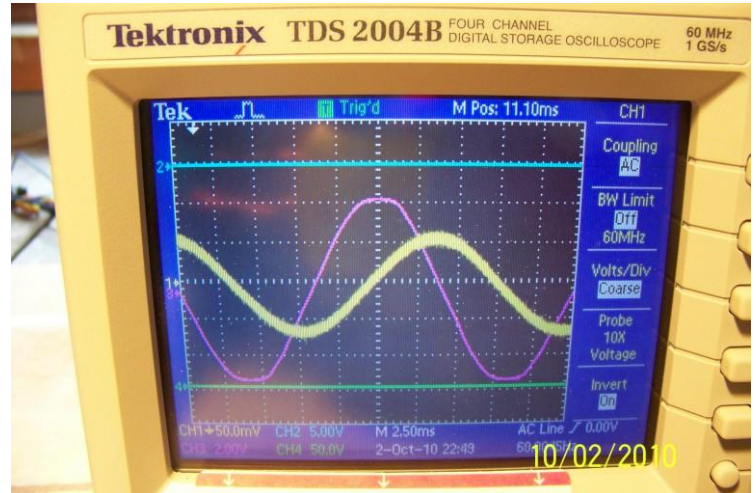


FIG 2.0 BITT T3
Low Voltage Sine Waves
Voltage Sine Wave = Purple
Current Sine Wave = Yellow

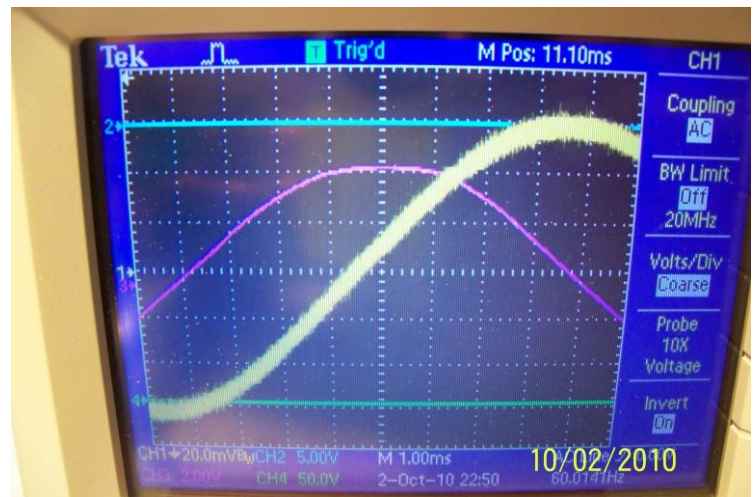


FIG 3.0 BITT T3
Close Up – Low Voltage Sine Waves
Power Factor = 0.17 @ 80 degrees

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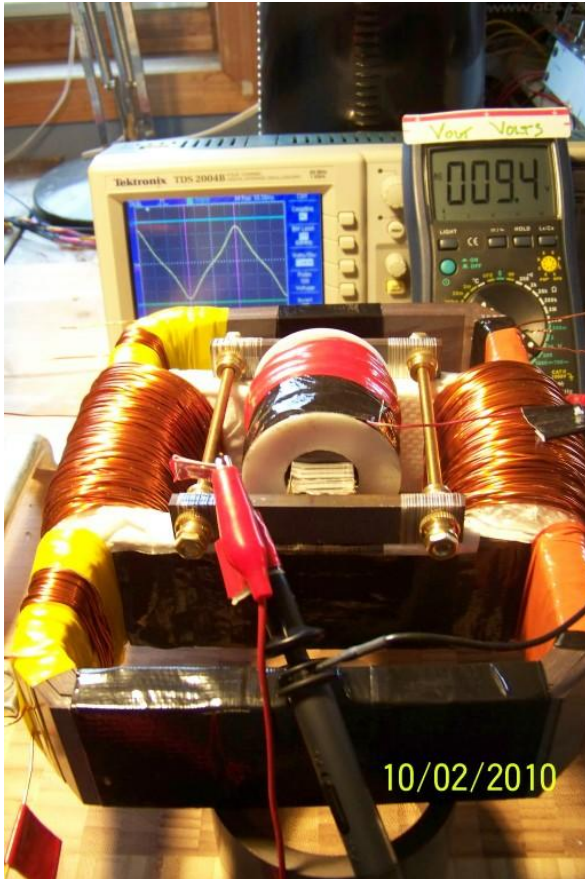


FIG 4.0 BITT T3
High Output Voltage
w/ Core Saturation

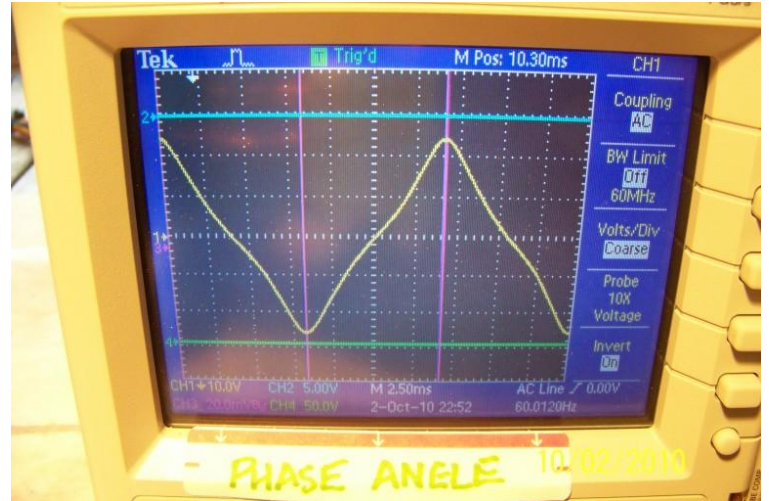


FIG 5.0 BITT T3
High Voltage Sine Waves
w/ Core Saturation

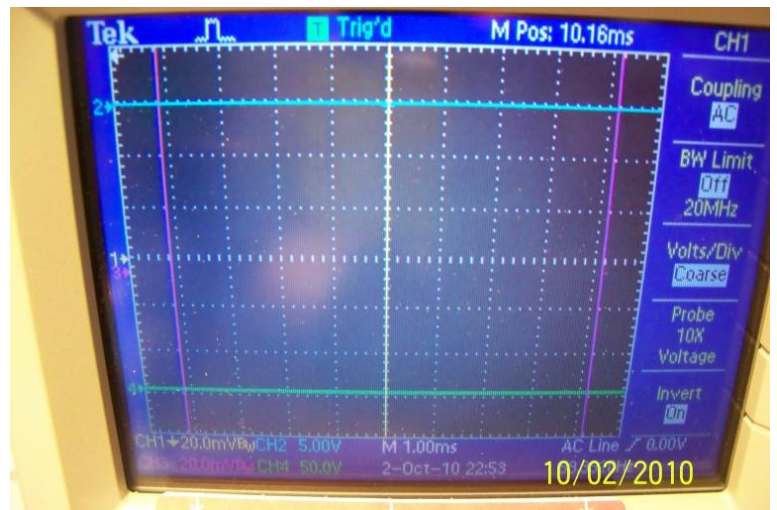


FIG 6.0 BITT T3
Close Up - High Voltage Sine Waves
w/ Core Saturation
Power Factor = 0 @ 90 degrees

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FIG 7.0 BITT T3
NO LOAD INPUT CURRENT
= 0.749 Amps

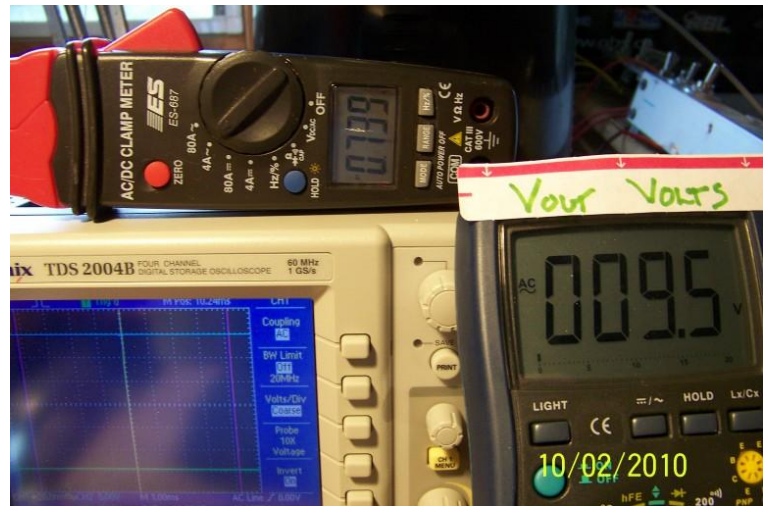


FIG 8.0 BITT T3
ON LOAD INPUT CURRENT
= 0.739 Amps

Test Conclusions:

Primary Coil Power Factor Reduction

Saturation of the Primary Coil's core reduced the Power Factor by 11.1% from a NO LAOD PF of 0.17 to ON LOAD PF of 0.0.

Primary Coil Input Current Reduction

While the Load Output Voltage increased by a factor of 9400%, the Primary Coil Input Current reduced by 1.34% or 10mA.

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Date: October 2nd, 2010